

PATENT NUMBER

M.H. O.I.P.E. SCANNED <i>[Signature]</i> Q.A. <i>[Signature]</i>	PATENT DATE <i>[Signature]</i>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/752998	F	324	705	3839	KARLSEN
APPLICANTS	Chang-Nyun Kim Sun-Ju Kim Jong-Hyun Kim Chung-Koo Yoon				
TITLE	Method and apparatus for testing semiconductor devices using the back side of a circuit board				

PTO-2040
 (28)

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____			ISSUE FEE	
	Amount Due	Date Paid		
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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